

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Docket No: Q76774

Hiroyuki HASEBE, et al.

Appln. No.: Unknown

Confirmation No.: Unknown

Group Art Unit: Unknown

Filed: August 5, 2003

Examiner: Unknown

For: DISCHARGE LAMP LIGHTING DEVICE

**INFORMATION DISCLOSURE STATEMENT
UNDER 37 C.F.R. §§ 1.97 and 1.98**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with the duty of disclosure under 37 C.F.R. § 1.56, Applicant hereby notifies the U.S. Patent and Trademark Office of the documents which are listed on the attached PTO/SB/08 A & B (modified) form and/or listed herein and which the Examiner may deem material to patentability of the claims of the above-identified application.

One copy of each of the listed documents is submitted herewith.

The present Information Disclosure Statement is being filed: (1) No later than three months from the application's filing date for an application other than a continued prosecution application (CPA) under §1.53(d); (2) Before the mailing date of the first Office Action on the merits (whichever is later); or (3) Before the mailing date of the first Office Action after filing a request for continued examination (RCE) under §1.114, and therefore, no Statement under 37 C.F.R. § 1.97(e) or fee under 37 C.F.R. § 1.17(p) is required.

In compliance with the concise explanation requirement under 37 C.F.R. § 1.98(a)(3) for foreign language documents, Applicant submits the following explanations:

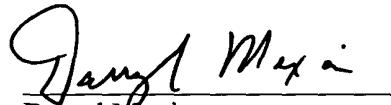
INFORMATION DISCLOSURE STATEMENT
U.S. Appln. No.: Q76774

JP-A 2002-159172 discloses a DC/DC converter circuit in which a primary winding of a transformer is connected to a secondary winding thereof via a capacitor, and it is possible to reduce the size of the transformer by alleviating a peak current in the transformer.

JP-A 2001-143890 discloses a discharge lamp lighting device which lights a discharge lamp connected to the output side of a direct current booster which boosts and outputs an input DC voltage through an inverter circuit.

The submission of the listed documents is not intended as an admission that any such document constitutes prior art against the claims of the present application. Applicant does not waive any right to take any action that would be appropriate to antedate or otherwise remove any listed document as a competent reference against the claims of the present application.

Respectfully submitted,



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WASHINGTON OFFICE
23373
CUSTOMER NUMBER

Date: August 5, 2003

<p>Substitute for Form 1449 A & B/PTO</p> <p>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</p> <p><i>(use as many sheets as necessary)</i></p>				<p><i>Complete if Known</i></p>	
				Application Number	Unknown
				Confirmation Number	Unknown
				Filing Date	August 5, 2003
				First Named Inventor	Hiroyuki HASEBE, et al.
				Art Unit	Unknown
				Examiner Name	Unknown
Sheet	1	of	1	Attorney Docket Number	Q76774

Examiner Signature _____ **Date Considered** _____

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹Applicant's unique citation designation number (optional). ²See Kind Codes of USPTO Patent Documents at www.uspto.gov, MPEP 901.04 or in the comment box of this document. ³Enter Office that issued the document, by the two-letter code (WIPO Standard ST. 3). ⁴For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶Applicant is to indicate here if English language Translation is attached.